

Notice of References Cited¹

Application/Control No.

09/998,532

Applicant(s)/Patent Under
Reexamination
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Examiner

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Art Unit

2182

Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,663,734	09-1997	Krasner	342/357
	B	US-5,901,074	05-1999	Nakano et al.	364/709
	C	US-5,960,035	09-1999	Sridhar et al.	375/219
	D	US-5,982,807	11-1999	Snell	375/200
	E	US-6,016,143	01-2000	Heinzman	345/339
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FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Virtex Pin Definitions, 07/19/2002, Xilinx, Version 2.8, Table 1 (Note, Virtex FPGA has been before 2001, see reference "V")
	V	Xilinx Ships World's Highest Density FPGA, Xilinx, 07/31/2000
	W	
	X	

¹A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.